

## Approval: 1<sup>st</sup> Convocation Adhoc Meeting

**Course Title:** Digital Electronic Circuits Lab.

**Course No.:** EE 208P

**Credits:** 0-0-4-2

**Prerequisites**

**Students intended for:** B.Tech.

**Elective or Compulsory**

**Semester** Odd/Even

**Course contents:**

**Experiment List:**

Lab 1: Introduction

Lab 2: Analysis of Logic circuits

Lab 3: Synthesis of Logic circuits

Lab 4: Design of one bit half adder & full adder and n bit binary ripple carry adder

Lab 5: Design of Adder-Subtractor circuit

Lab 6: Interfacing with seven segment display

Lab. 7: Latches and Flip Flops

Lab 8 : Design of Synchronous and Asynchronous counter

Lab 9 : Design of a four bit Jhonson Counter. (the outputs of the flip flops are decoded to generate the eight timing signals)

Lab 10: Design of four bit bidirectional shift register