

BMBF TU9-IIT Mandi Workshop on  
"Current Trends in Analog Circuit Designing"

September 25-26, 2017

As a part of TU9-IIT Mandi exchange programme, IIT Mandi and TU-Berlin have organized a BMBF funded workshop on "Current Trends in Analog Circuit Designing" on september 25-26, 2017. The workshop was organized by Dr. Hitesh Shrimali of IIT Mandi and Prof. Friedel Gerfers of TU-Berlin.

The workshop included experts from semiconductor industries such as STMicroelectronics, Synopsys and Western Digital (Sandisk). Prof. Friedel Gerfers presented high performance, high precision, energy efficient sigma-delta data conversion techniques. Moreover, Prof. Friedel Gerfers has presented measurement results of his recent IEEE publication with his PhD student Marcel Runge. Dr. Jai Narayan Tripathi (Technical Leader, STMicroelectronics) has delivered a lecture on jitter measurement techniques. He has presented his recent publications with measurement results on CMOS and FD-SOI technologies. Mr. Tapas Nandi (Director, Synopsys pvt. ltd.) has delivered expert talk on high speed wireline communication links, keeping the focus on high speed transmission links, PLL, CDR and circuits. Mr. Nitin Gupta (Director, Western Digital) has presented the world of analog in high performance system on chips. Mr. Atul Bhargava (Technical Staff Engineer, STMicroelectronics) has presented design methodologies for state of the art analog circuits.

The event was co-ordinated by student co-ordinators: Vijender Kumar Sharma, Kumar Sambhav Pandey, Sumit Kumar Pandey, Puneet Arora, Ashish Joshi, Dinesh Kumar B., Indu Yadav, Shivani Sharma, and Ankita Deo.

The detailed workshop activities can be found on the following link:

<http://iitmandi.ac.in/workshops/CTACD/>