

No. IITM /SRIC/Project No.436

Date: 03.06.2025.

**Advertisement No.: IITM/SRIC/Project No. IITM/Meity/HS/436/2025-01**

Applications from Indian nationals are invited under the following project on temporary basis with consolidated salary. The renewal of services for few months/a year or up to the duration of the project will be based on satisfactory performance. The initial appointment will be for a period of 1 year or the end of the project (whichever is earlier).

| <b>Title of the Project</b>                   | ASIC and package design of ultra-small atomic clock |   |
|---|---|---|
| <b>Funding Agency</b>                         | MeitY   |   |
| <b>Name(s) of the Project Investigator(s)</b> | Prof. Hitesh Shrimali                               |   |
| <b>School/Center</b>                          | School of Computing and Electrical Engineering      |   |
| <b>Duration of the Project</b>                | In months: 60                                       | End date: (07.08.2028)  |
| <b>Post (s)</b>                               | <b>Consolidated Pay-slab / Salary</b>               | <b>Qualifications</b>   |
| Senior Project Associate-I                    | 47,166/- per month                                  | <b>Essential:</b><br>M.Tech in relevant specialization of EC/EE/IN with GATE score in EC/EE/IN disciplines<br>or<br>B.Tech. (EC/EE/Instrumentation) with a GATE score in EC/EE/IN discipline<br><br>Age Limit: The upper age limit is 35 years.<br><br><b>Desirable</b> <ul style="list-style-type: none"> <li>Should have good understanding on following UG level subjects: <ul style="list-style-type: none"> <li>Analog Circuits</li> <li>Digital Circuits</li> <li>Signals and Systems</li> <li>Control Theory</li> </ul> </li> <li>Must have experience of using Linux operating system,</li> <li>Hand-on experience on MATLAB and VLSI IC design tools such as Cadence is add-on.</li> </ul> |
| Project Associates I and II                   | 36,250/- per month                                  |   |
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The candidates should submit their application with complete CV (having personal details, academic qualifications, research experience, publications, name, affiliation and mobile number, email address of two referees, information regarding educational qualifications indicating percentage of marks/division, details of work experience, etc. as pdf attachment **by e-mail with advertisement No. on the subject line to the PI, Dr. Hitesh Shrimali, Professor, at email id: ...[hitesh@iitmandi.ac.in](mailto:hitesh@iitmandi.ac.in)**.

IIT Mandi reserves the right to fix higher criteria for shortlisting of eligible candidates from those satisfying advertised qualifications and requirement of the project post. The name of the shortlisted candidates will be either displayed on website or communicated individually along with **the offline/online interview details**.

Relaxation of marks may be granted to the SC/ST Candidates as per GoI norms. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing GoI rules/norms. **The last date for submitting the completed applications by email is June 25, 2025 (11:59 pm).** In case any clarification is required on eligibility regarding the above post, the candidate **may contact at email id: [hitesh@iitmandi.ac.in](mailto:hitesh@iitmandi.ac.in)**.